

ABSTRACT OF THE DISCLOSURE

An information processing device for efficiently processing the VLIW instructions is disclosed. The information processing device includes an $m \times n$ (m -row \times n -column) instruction buffer, a plurality of instruction executing parts executing a plurality of instructions in parallel, and a control circuit for selecting a predetermined number of instructions from the $m \times n$ instruction buffer and distributing the instructions to the instruction executing parts.

[illegible]